

Notice of References Cited

Application/Control No.

09/384,504

Applicant(s)/Patent Under
Reexamination
MARSHALL ET AL.

Examiner

Samuel Broda

Art Unit

2123

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Blaquière et al, "Timing Analysis Speed-up Using a Hierarchical and a Multimode Approach," IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 15 No. 2, pp. 244-255 (February 1996)
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